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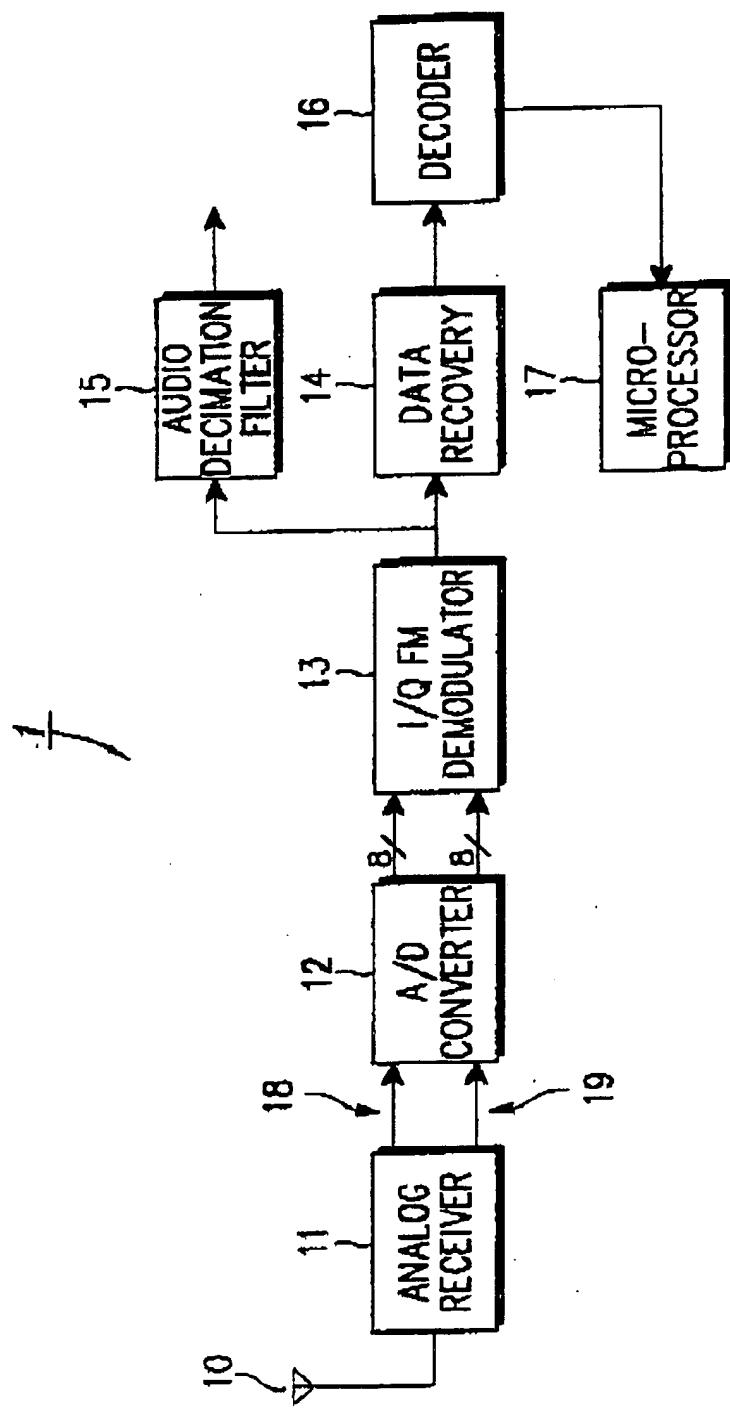


FIG. 1
(PRIOR ART)

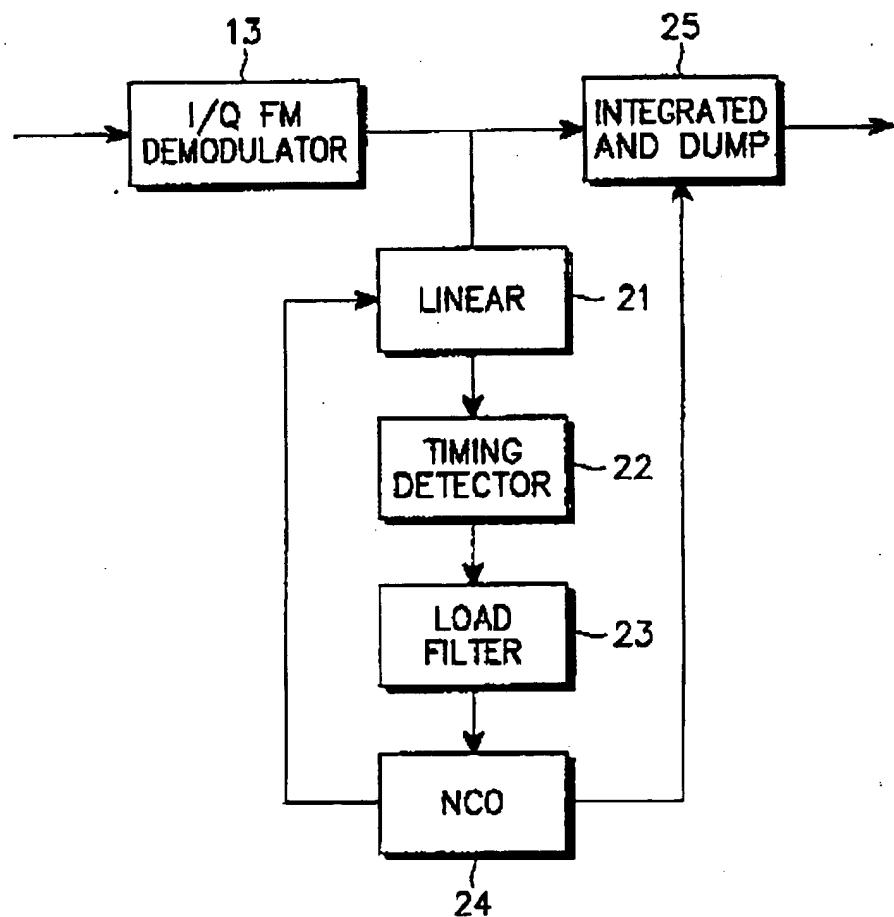


FIG.2
(PRIOR ART)



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DOTTING SEQUENCE	WORD SYNC	MESSAGE WORD	DOTTING SEQUENCE	WORD SYNC	MESSAGE WORD	DOTTING SEQUENCE	WORD SYNC	MESSAGE WORD
SIGNAL 1 (101 BIT)	SIGNAL 1 (11 BIT)	BLOCK 1 (40 BIT)	SIGNAL 1 (37 BIT)	SIGNAL 2 (37 BIT)	BLOCK 2 (11 BIT)	SIGNAL 11 (37 BIT)	SIGNAL 11 (11 BIT)	BLOCK 11 (40 BIT)

DOTTING SEQUENCE SIGNAL: 10101010~
WORD SYNC SIGNAL: 111000110010
MESSAGE WORD BLOCK: BCH CODED 28BIT MESSAGE STRING

FIG.3
(PRIOR ART)



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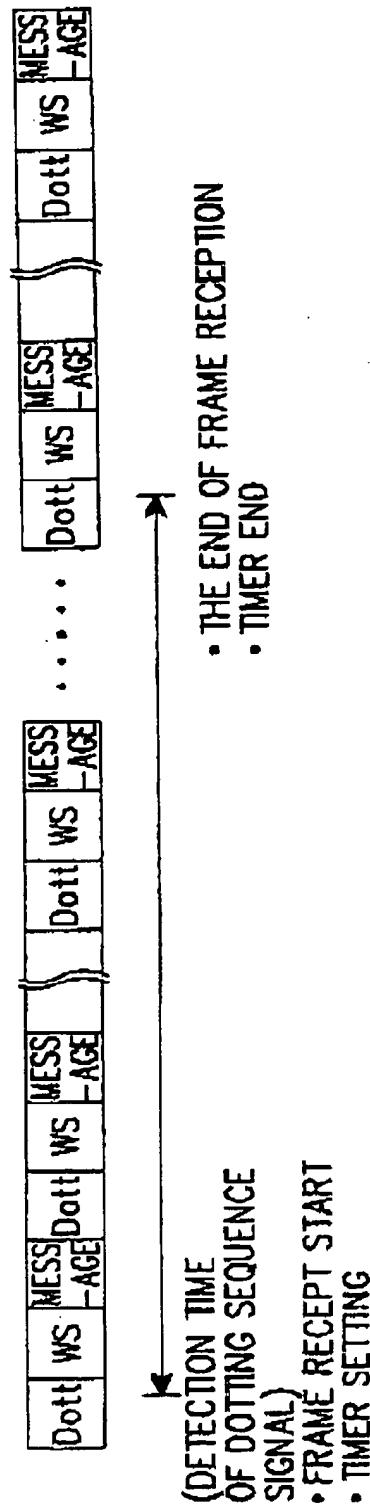
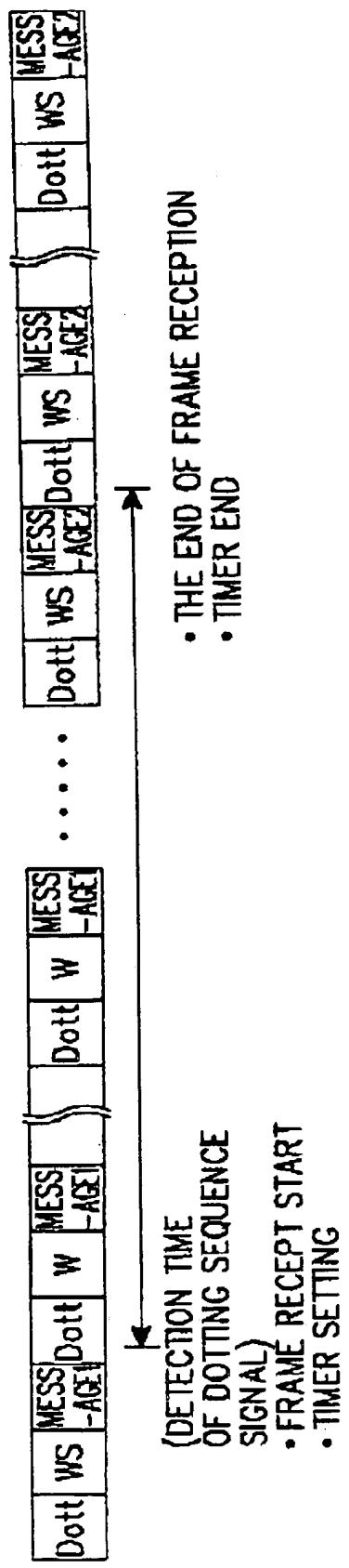


FIG. 4
(PRIOR ART)



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- THE END OF FRAME RECEPTION
- TIMER END
- FRAME RECEP'T START
- TIMER SETTING

FIG.5
(PRIOR ART)